



DEAN OF DIGITAL INFRASTRUCTURE AND AUTOMATION
INDIAN INSTITUTE OF TECHNOLOGY (IIT), KANPUR
KANPUR (U.P.) 208016

OFFICE ORDER

No. DDIA/IITK/2020/OO-03/1121
20-01-2020

The following High Performance Computing Executive Committee (HPCEC) is constituted to monitor and direct all activities in Computer Center with regards to High Performance Computing Clusters, infrastructure development and maintenance.

1. Prof. Sumit Ganguly, DDIA (chair)
2. Prof. Jayant K. Singh (CHE)
3. Prof. Amalendu Chandra (CHM)
4. Prof. Sankar Ramakrishnan (BSBE)
5. Prof. Nisanth Nair (CHM)
6. Prof. Amey Karkare (CSE)

This has the authorization of the competent authority.

Sincerely

Sumit Ganguly

(Sumit Ganguly)
DDIA

21.1.2020

Cc:

1. Prof. Sumit Ganguly, DDIA
2. Prof. Jayant K. Singh, CHE
3. Prof. Amalendu Chandra, CHM
4. Prof. Sankar Ramakrishnan, BSBE
5. Prof. Nisanth Nair, CHM
6. Prof. Amey Karkare, CSE

Dr. Brajesh Pande FYI.