

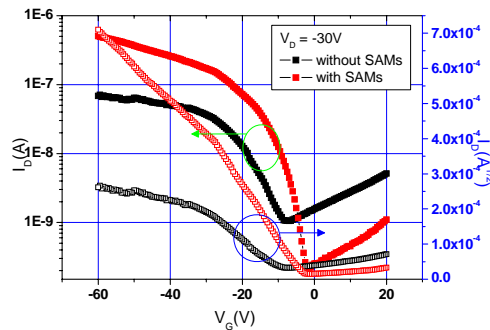
## Characteristic Improvements for Bottom Contact OTFTs with Self-Assembled Monolayer Formed by ALD

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### Abstract

The interaction of  $\pi$ -conjugated pentacene layer with metal contact or gate dielectric surfaces strongly affects the growth process and determines the morphology of the organic film. In particular, in bottom contact configuration the polycrystalline pentacene film degrades in an amorphous phase along the boundaries of the source and drain contacts, decreasing the performance of the devices. It has been demonstrated that the interaction of the  $\pi$  system with the interfaces can be decreased by covering the surface with a monolayer of octyltrichlorosilane, before the deposition of pentacene. One of critical factors for high performance of OTFTs is the properties of the interface between semiconductor and gate insulator, because the growth of pentacene molecules is strongly affected by the deposition conditions and the surface properties of gate insulators or substrates. It is known that the vertical alignment of pentacene molecules provides a strong  $\pi$ - $\pi^*$  overlap and can increase the electrical conductivity in the direction perpendicular to the long axis of pentacene molecules. The hydrophilic surface polarity of PVP layer prior to the deposition of pentacene can deteriorate the upright growth of pentacene molecules to the substrate. In this work, octyltrichlorosilane was deposited onto PVP layer prior to the deposition of pentacene to modify the surface polarity of PVP by ALD method. The surface polarity of the gate insulator became more hydrophobic by the SAM on the PVP layer. The change of the surface polarity induces the molecular ordering of the pentacene film deposited on the SAM, which enhances the charge channel formation and transport of the carriers. The electrical characteristics of the fabricated OTFTs are shown in Fig. 1. For the device with the SAM layer,  $\mu_{FE}=1.17 \times 10^{-2} \text{ cm}^2/\text{V s}$ ,  $V_{th}=-2 \text{ V}$ , and on/off current ratio of  $\sim 10^3$  were extracted, while the device without the SAM layer presented  $\mu_{FE}=2.7 \times 10^{-3} \text{ cm}^2/\text{V s}$ ,  $V_{th}=-8 \text{ V}$ , and on/off current ratio of  $\sim 10^2$ .



**Figure1.** Transfer curve in the saturated regime at a constant source-drain voltage of -30V